



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No.: 10/803,599 Filing Date: March 17, 2004  
Confirmation No.: 7371  
First Named Inventor: Yi Ding  
Assignee: ProMOS Technologies, Inc.  
Examiner: Dang, Phuc T. Art Unit: 2818  
Attorney Docket No.: M-15204-1D US

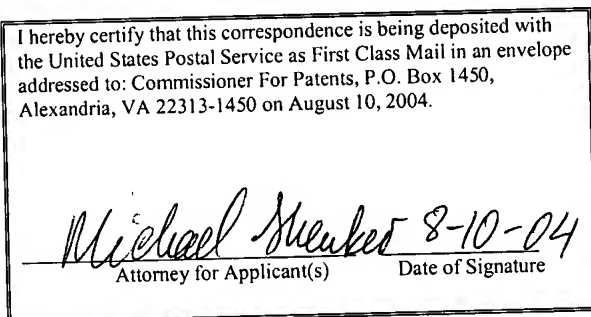
San Jose, California  
August 10, 2004

Mail Stop Issue Fee  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**REQUEST TO RETURN INITIALED FORM PTO-1449**

Dear Sir:

Enclosed is a copy of 1 Sheet of Form PTO-1449 submitted with Information Disclosure Statements dated April 16, 2004, for the above-referenced patent application. Applicants have not received a copy of the form initialed by the Examiner. Applicants request the form to be initialed by the Examiner and returned to the undersigned.



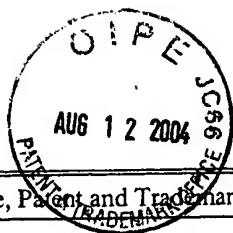
Respectfully submitted,

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Application No. 10/803,599



April 16, 2004

Sheet 1 of 1

U.S. Department of Commerce, Patent and Trademark Office		Atty Docket No.	Serial No.
		M-15204-1D US	10/803,599
INFORMATION DISCLOSURE STATEMENT BY APPLICANT		Applicant	
(Use several sheets if necessary)		Yi Ding	
		Filing Date	Group
		March 17, 2004	Unassigned

U.S. Patent Documents							
*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
	AA	6,420,231	16 Jul. 2002	Harari et al.			
	AB	2003/0218908 A1	27 Nov. 2003	Park et al.			
	AC	2004/0004863 A1	8 Jan. 2004	Wang			
	AD						
	AE						
	AF						
	AG						
	AH						

Foreign Patent Documents								
							Translation	
		Document	Date	Country	Class	Subclass	Yes	No
	AI	EP 0 938 098 A2	25 Aug. 1999	Europe				
	AJ							
	AK							
	AL							

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)		
AM	United States Patent Application No. 10/798,475, entitled "Fabrication of Conductive Lines Interconnecting Conductive Gates in Nonvolatile Memories and Non-Volatile Memory Structures," Filed on March 10, 2004; Attorney Docket No. M-15296 US.	
AN	United States Patent Application No. 10/797,972, entitled "Fabrication of Conductive Lines Interconnecting First Conductive Gates in Nonvolatile Memories Having Second Conductive Gates Provided By Conductive Gates Lines, Wherein The Adjacent Conductive Gate Lines For The Adjacent Columns Are Spaced From Each Other, And Non-Volatile Memory Structures," Filed on March 10, 2004; Attorney Docket No. M-15297 US.	
AO		
AP		

Examiner	Date Considered
<p>*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication to applicant.</p>	